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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10087432	03/01/2002	257	177	2811	<i>Michael</i>

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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

TAIWAN 90104 '84 03/01/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			U 013887-9
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			
Verified and Acknowledged <input type="checkbox"/> examiners's initials			
TITLE : Stacked semiconductor chip package			

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE		Total Claims	Print Claim for O.G.
Amount Due	Date Paid	DRAWING	
<input type="checkbox"/> TERMINAL DISCLAIMER		Sheets Drwg.	Figs. Drwg.
		Print Fig. <i>2C</i>	
Assistant Examiner		Application Examiner	
Primary Examiner		PREPARED FOR ISSUE	
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